

L Number	Hits	Search Text	DB	Time stamp
1	9474	pixel near3 array	USPAT	2003/08/22 21:09
2	103031	silicon near3 substrate\$1	USPAT; EPO; JPO	2003/08/22 21:49
3	15374	memory near3 module	USPAT; EPO; JPO	2003/08/22 21:10
4	1	intergac\$3 near3 control\$4	USPAT; EPO; JPO	2003/08/22 21:10
5	47923	sampl\$3 near3 circuit\$1	USPAT; EPO; JPO	2003/08/22 21:11
6	992	(pixel near3 array) and (silicon near3 substrate\$1)	USPAT; EPO; JPO	2003/08/22 21:11
7	9	(memory near3 module) and ((pixel near3 array) and (silicon near3 substrate\$1))	USPAT; EPO; JPO	2003/08/22 21:13
8	426	(liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1))	USPAT; EPO; JPO	2003/08/22 21:15
9	30	(memory near3 control\$4) and ((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1)))	USPAT; EPO; JPO	2003/08/22 21:50
10	30	(sampl\$3 near3 circuit\$1) and ((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1)))	USPAT; EPO; JPO	2003/08/22 21:16
11	8	((memory near3 control\$4) and ((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1)))) and ((sampl\$3 near3 circuit\$1) and ((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1))))	USPAT; EPO; JPO	2003/08/22 22:03
12	86140	silicon adj3 substrate\$1	USPAT; EPO; JPO	2003/08/22 21:49
13	10264	memory near3 pixel\$1	USPAT; EPO; JPO	2003/08/22 21:51
14	245	(silicon adj3 substrate\$1) and (memory near3 pixel\$1)	USPAT; EPO; JPO	2003/08/22 21:51
15	25	((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1))) and ((silicon adj3 substrate\$1) and (memory near3 pixel\$1))	USPAT; EPO; JPO	2003/08/22 21:51
16	78	(pixel near3 array) and ((silicon adj3 substrate\$1) and (memory near3 pixel\$1))	USPAT; EPO; JPO	2003/08/22 21:52
17	25	((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1))) and ((pixel near3 array) and ((silicon adj3 substrate\$1) and (memory near3 pixel\$1)))	USPAT; EPO; JPO	2003/08/22 21:52
18	2	(low near3 power) and (((memory near3 control\$4) and ((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1)))) and ((sampl\$3 near3 circuit\$1) and ((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1)))))	USPAT; EPO; JPO	2003/08/22 22:57
19	0	(Pixel\$1 near3 cell) with (static near2 RAM near3 cell)	USPAT; EPO; JPO	2003/08/22 22:58
20	0	Pixel\$1) near5(static near2 RAM near3 cell	USPAT; EPO; JPO	2003/08/22 22:58
21	8294	static near2 RAM	USPAT; EPO; JPO	2003/08/22 22:59

22	761	static near2 RAM near3 cell	USPAT;	2003/08/22
23	2867	pixel near3 cell	EPO; JPO	22:59
24	0	(static near2 RAM near3 cell) with (pixel near3 cell)	USPAT;	2003/08/22
25	15	(static near2 RAM near3 cell) and (pixel near3 cell)	EPO; JPO	22:59
26	0	(static near2 RAM near3 cell) and ((liquid adj crystal adj display or lcd) and ((pixel near3 array) and (silicon near3 substrate\$1)))	USPAT;	2003/08/22
27	0	pixel with (static near2 RAM near3 cell)	EPO; JPO	23:03
			USPAT;	2003/08/22
			EPO; JPO	23:04